

MEDIA ALERT



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SmartDV's DVCon China Exhibit to Showcase Extensive Verification IP Portfolio
*Will Highlight New TileLink Verification IP for RISC-V Systems,
SimXL Synthesizable Transactors for Emulation, FPGA Prototyping*

SAN JOSE, CALIF — April 10, 2019 —

WHO: [SmartDV™ Technologies](#), the **Proven** and **Trusted** choice for Verification IP

WHAT: Will highlight TileLink Verification IP for RISC-V based systems and SimXL, Synthesizable Transactors for accelerating system-level SoC testing on hardware emulators and FPGA prototyping platforms at [DVCon China](#) in Booth #110.

Also highlighted will be its large portfolio of Verification IP used at more than 120 leading networking, storage, wireless and memory companies, and support for all commercial simulators, emulators, formal tools and verification languages.

WHEN: Wednesday, April 17

WHERE: Crowne Plaza Hotel Century Park in Shanghai, China

About SmartDV

[SmartDV™ Technologies](#), the **Proven** and **Trusted** choice for Verification IP, offers high-quality standard and custom protocol Verification IP supporting all

simulation, emulation, formal verification tools and verification languages used in a coverage-driven chip design verification flow. Design groups worldwide rely on SmartDV's Verification IP solutions developed by talented and experienced ASIC and SoC design verification engineers for verification of their networking, storage, automotive, bus, MIPI and display chip projects. The result is Proven and Trusted Verification IP used in hundreds of projects throughout the global electronics industry. SmartDV is headquartered in Bangalore, India, with U.S. headquarters in San Jose, Calif. Visit www.SmartDV.com to learn more.

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