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**SmartDV Appoints HyperSilicon Exclusive Sales Representative in China**  
*Will Provide Sales Support for SmartDV's Design, Verification IP; Partner to Integrate SimXL Synthesizable Transactors with Desktop Emulation, FPGA Prototyping Tools*

**SAN JOSE, CALIF — May 9, 2019 — [SmartDV™ Technologies](#), the **Proven** and **Trusted** choice for Verification intellectual property (IP), today named HyperSilicon Co. Ltd. its exclusive sales representative in China.**

HyperSilicon is a technical sales and support organization based in Beijing, China, with a 30-member team of IP, system-on-chip (SoC), application specific integrated circuit (ASIC) and field-programmable (FPGA) design experts. Working with SmartDV, HyperSilicon will provide sales and technical support for SmartDV's extensive portfolio of Design and Verification IP throughout China.

Additionally, SmartDV and HyperSilicon will work together to integrate the SEMU desktop emulator and VeriTiger product line, a family of field programmable gate array (FPGA) prototyping solutions, with SmartDV's SimXL™ portfolio of Synthesizable Transactors.

SimXL, a configurable, reusable plug-and-play verification solution for interfaces based on industry-standard hardware verification languages (HVLs), is used with hardware emulation or rapid prototyping tools to accelerate system-level, SoC testing. It is fully compliant with popular standards' specifications, including automotive, serial bus, memory, MIPI, networking, SoC interconnect fabrics, storage and video protocols. An interface based on UVM, OVM, SystemVerilog, SystemC controls SimXL.

“HyperSilicon is well respected and has the added advantage of desktop emulation and FPGA-based rapid prototype boards that complement our SimXL synthesizable transactors,” remarks Barry Lazow, vice president of sales for SmartDV. “We welcome the opportunity to work cooperatively with HyperSilicon to improve chip design validation and verification.”

Adds Daniel Bai, vice president of HyperSilicon: “SmartDV is known for having the largest portfolio and highest quality design and verification IP solutions, along with exceptional customer support. It is destined to become a valuable resource to our design and verification community.”

## **About SmartDV**

[SmartDV™ Technologies](#) is the Proven and Trusted choice for Verification and Design IP with the best customer service from more than 250 experienced ASIC and SoC design and verification engineers. Its high-quality standard or custom protocol Verification and Design IP is compatible with all verification languages, platforms and methodologies supporting all simulation, emulation and formal verification tools used in a coverage-driven chip design verification flow. The result is ***Proven and Trusted*** Verification and Design IP used in hundreds of networking, storage, automotive, bus,

MIPI and display chip projects throughout the global electronics industry. SmartDV is headquartered in Bangalore, India, with U.S. headquarters in San Jose, Calif. Visit

[www.Smart-DV.com](http://www.Smart-DV.com) to learn more.

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